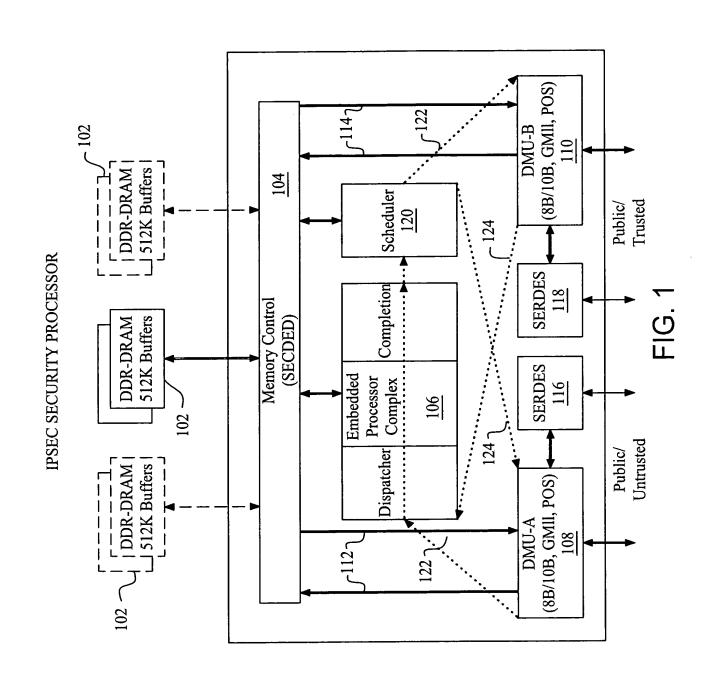
SHEET 1/7

INVENTOR(S): SANTOSH P. GAUR ET AL. DOCKET NO. RPS9-2002-0014 - SWR



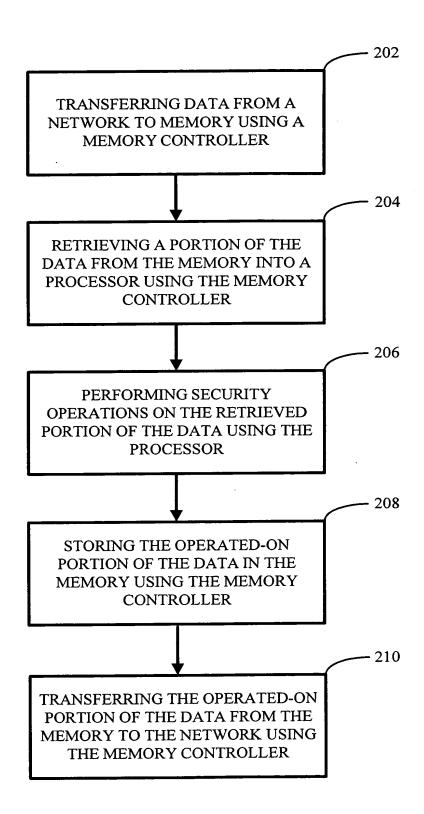


FIG. 2

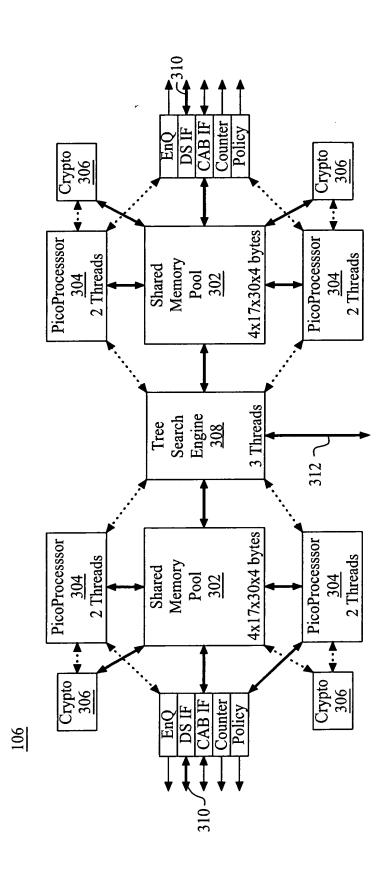


FIG. 3

Coprocessor Processor 406 MTU Fragmentation 406 "SA" Lookup Engine 402 "SA" Lookup Engine 402 (HMAC-SHA-1-96) (HMAC-MD5-96) Fragment Reassembly (3DES-CBC) Authentication (DES-CBC) (AES-CBC) Encryption , Padding , 110 108 [1 Gigabit or 10 10/100] Ethernet/POS MAC [1 Gigabit or 10 10/100] Ethernet/POS MAC **410** → 412 IPSEC PROCESSING Engine 414 Public/Untrusted (bump-in-the-wire) Public/Trusted Public Key 412 (HMAC-SHA-1-96) (HMAC-MD5-96) Authentication - 406 406 MTU Fragmentation 406 "SA" Lookup Engine 402 Packet Filter Engine 404 408 Fragment Reassembly Fragment Reassembly Anti-reply Control (3DES-CBC) (DES-CBC) Decryption (AES-CBC) 410>

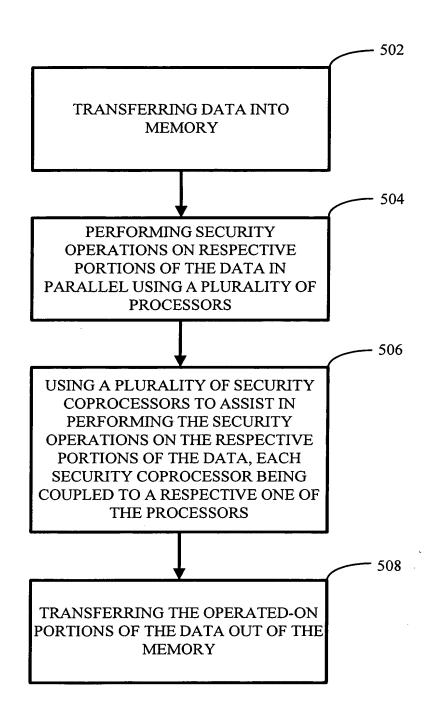
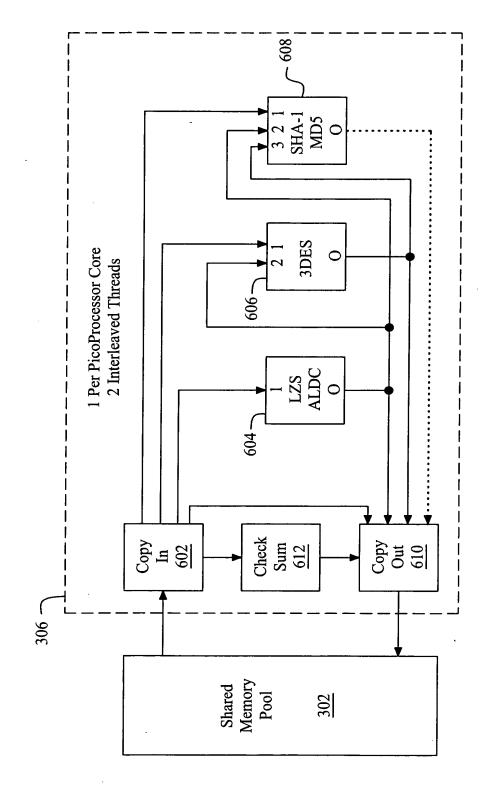


FIG. 5



CRYPTO CO-PROCESSOR BLOCK

FIG. 6

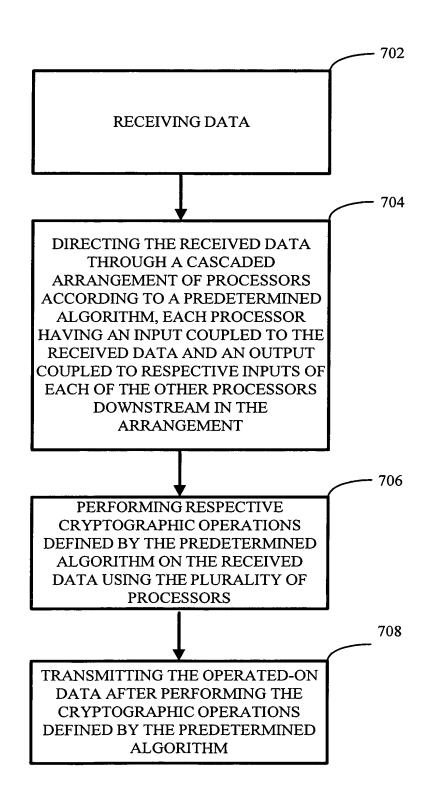


FIG. 7